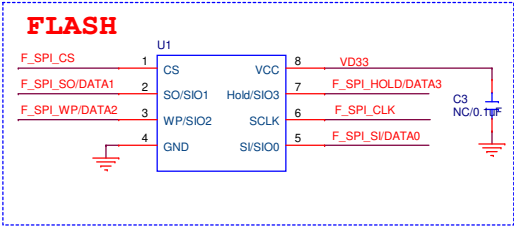
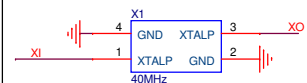


3.3V from SPS / SPS311

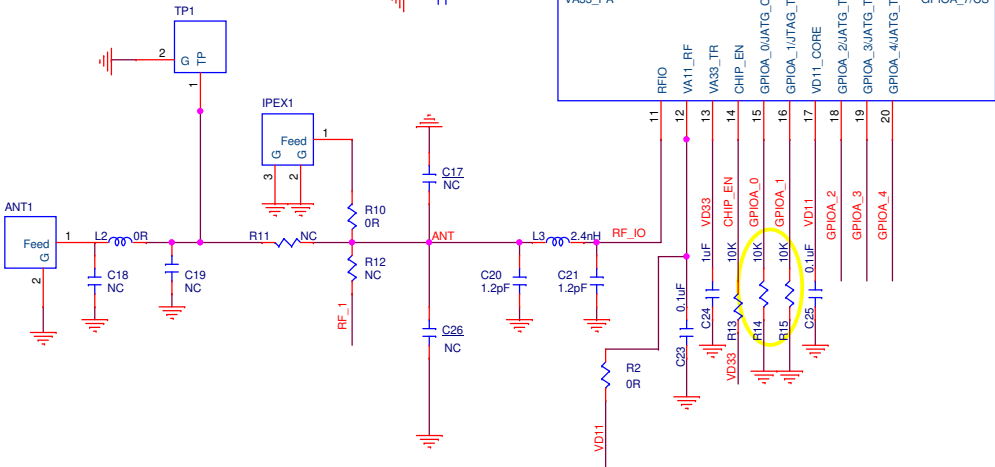
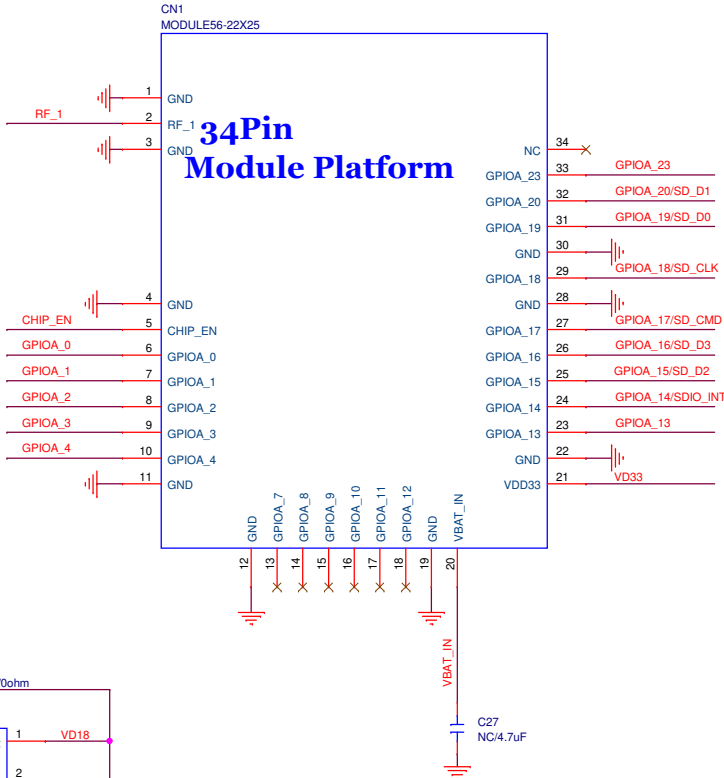
GPIOA_14 must reserve
PCB footprint JUMP for
BT FW debug use

Xtal function

The diagram shows a blue rectangular block representing the XTAL function. It has four pins: Pin 1 (bottom left) is labeled '1' and 'XTALP', connected to a red line labeled 'XI'. Pin 2 (bottom right) is labeled '2' and 'GND', connected to a red line with a ground symbol. Pin 3 (top right) is labeled '3' and 'XTALP', connected to a red line labeled 'XO'. Pin 4 (top left) is labeled '4' and 'GND', connected to a red line with a ground symbol. The block is labeled 'X1' at the top and '40MHz' at the bottom.



	L1
SWR MODE	2.2uH
LDO MODE	0ohm



RTL8720CM_QFN40